

Application Number

09/186,388

B.H. Lee et al.

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2814

(Use several sheets if necessary)

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

99	S. Wolf et al., "Silicon Processing for the VLSI Era, Volume 2: Process Integration", Lattice Press, Sunset Beach, California, c. 1990, pp. 332-334.

Guillermo

10/30/01

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through